STIC Search Report
EIC 3600

STIC Database Tracking Number: 107[illegible]

Joseph Rodriquez
Location Knx -3A11
Art Unit: 3653
Employee #78253
2-3692
09/687828

From: Etelka R. Griffin
Location: EIC 3600
KNOX/4B68
Phone:571-272-4230

Etelka.griffin@uspto.gov

Search Notes

Databases Searched: LEXIS/NEXIS
DIALOG
COURTLINK

Litigation was not found on Ser# 09/687828.

Application Number Information

Application Number: **09/687828**
Assignments
Filing or 371(c) Date: **10/12/2000** eDan
Effective Date: **10/12/2000**
Application Received: **10/16/2000**
Patent Number:
Issue Date: **00/00/0000**
Date of Abandonment: **00/00/0000**
Attorney Docket Number: **MSX 302RI**
Status: **80 /RESPONSE AFTER FINAL ACTION FORWARDED TO EXAMINER**
Confirmation Number: **9910**
Title of Invention: **PORTABLE TROMMEL**

Examiner Number: **78253 / RODRIGUEZ, JOSEPH**
Group Art Unit: **3653**
Class/Subclass: **209/241.000**
Lost Case: **NO**
Interference Number:
Unmatched Petition: **NO**
L&R Code: Secrecy Code:**1**
Third Level Review: **NO**
Oral Hearing: **NO**

IFW Madras
Waiting for Response Desc.
Amndt.aftr final
Secrecy Order: **NO**
Status Date: **05/26/2010**

Bar Code	PALM Location	Location Date	Charge to Loc	Charge to Name	Employee Name	Location
09687828ZA	28C1	05/15/2009	No Charge to Location	No Charge to Name	VOUDOM	RND/00/A 41
09687828ZB	28C1	05/15/2009	No Charge to Location	No Charge to Name	VOUDOM	RND/00/A 41
09687828ZC	28C1	05/15/2009	No Charge to Location	No Charge to Name	VOUDOM	RND/00/A 41

Appln Info | Contents | Petition Info | Atty/Agent Info | Continuity/Reexam | Foreign Data

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Continuity/Reexam Information for 12/615014

Parent Data

12615014, filed 11/09/2009 is a reissue of 11306185, filed 12/19/2005 ,now U.S. Patent #7295151

Child Data

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Application Number Information

Application Number: **09/687828**
Assignments
Filing or 371(c) Date: **10/12/2000** eDan
Effective Date: **10/12/2000**
Application Received: **10/16/2000**
Patent Number:
Issue Date: **00/00/0000**
Date of Abandonment: **00/00/0000**
Attorney Docket Number: **MSX 302RI**
Status: **80 /RESPONSE AFTER FINAL ACTION FORWARDED TO EXAMINER**
Confirmation Number: **9910**
Title of Invention: **PORTABLE TROMMEL**

Examiner Number: **78253 / RODRIGUEZ, JOSEPH**
Group Art Unit: **3653**
Class/Subclass: **209/241.000**
Lost Case: **NO**
Interference Number:
Unmatched Petition: **NO**
L&R Code: Secrecy Code:**1**
Third Level Review: **NO**
Oral Hearing: **NO**

IFW Madras
Waiting for Response Desc.
Amndt.aftr final
Secrecy Order: **NO**
Status Date: **05/26/2010**

Bar Code	PALM Location	Location Date	Charge to Loc	Charge to Name	Employee Name	Location
09687828ZA	28C1	05/15/2009	No Charge to Location	No Charge to Name	VOUDOM	RND/00/A 41
09687828ZB	28C1	05/15/2009	No Charge to Location	No Charge to Name	VOUDOM	RND/00/A 41
09687828ZC	28C1	05/15/2009	No Charge to Location	No Charge to Name	VOUDOM	RND/00/A 41

Appln Info | Contents | Petition Info | Atty/Agent Info | Continuity/Reexam | Foreign Data

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Attorney Docket # [ ] Search

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http://EXPOWEB1:8001/cgi-bin/expo/GenInfo/snquery.pl?APPL_ID=09687828

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Source: Legal > Area of Law - By Topic > Patent Law > Find Patents > More U.S. Patents > **Utility, Design and Plant Patents**
Terms: **patno=7295151** (Edit Search)

306185 (11) 7295151 November 13, 2007 ,

UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT

7295151

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June 21, 2007 ,

Systems and methods for self-test of a radar , , altimeter

REISSUE:
November 9, 2009 - Reissue Application filed, Ex. Gp.: 3662; Re. S.N. 12/615,014 , , (O.G. December 8, 2009) ,

APPL-NO: 306185 (11)

FILED-DATE: December 19, 2005

GRANTED-DATE: November 13, 2007 ,

ASSIGNEE-PRE-ISSUE:
December 19, 2005 - ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS)., HONEYWELL INTERNATIONAL INC. 101 COLUMBIA ROAD MORRISTOWN NEW JERSEY 07962, Reel and Frame Number: 017485/0255

ASSIGNEE-AT-ISSUE:
Honeywell International Inc., Morristown, NEW JERSEY, United States of America (US), United States company or corporation (02)

CORE TERMS: frequency, radar, altimeter, switch, clock, phase, dump, integrator, voltage, tick, comparator, capacitor, detector, sample, divider, integration, transmitter, threshold, resistor, digital, mixer, sampled, sends, open position, malfunctioning, predefined, curve, self-testing, transmission, integrated

ENGLISH-ABST:

Systems and methods for testing a signal generated by a Direct Digital Synthesizer (DDS) in a radar altimeter. In an embodiment of the method, a voltage signal derived by comparing a fixed reference frequency to a ramped frequency signal generated by the DDS based on a clock-based reference signal is generated. The generated voltage signal is integrated over a

predefined range of clock signals. The integration is sampled at a previously defined clock tick. The sample is compared to a desired value and an indication that the radar altimeter is malfunctioning is provided if the comparison exceeds a predefined threshold value. The radar altimeter system is deactivated if an indication that the radar altimeter is malfunctioning has been provided.

Source: Legal > Area of Law - By Topic > Patent Law > Find Patents > More U.S. Patents > **Utility, Design and Plant Patents**
Terms: **patno=7295151** (Edit Search)
View: **Custom**
Segments: Abst, Assignee, English-abst, Expiration-date, Filed-date, Granted-date, Pct-filed-date, Priority, Publication, Reexam-litigate, Reissue
Date/Time: Thursday, June 17, 2010 - 2:37 PM EDT

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Terms: 7295151 or 7,295,151 (Edit Search)

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1. US Fed News, November 14, 2007 Wednesday 1:05 AM EST, , 267 words, Washington Inventor Develops Radar Altimeter Self-Testing Method, US Fed News, Alexandria, Va.

   CORE TERMS: signal, altimeter, patent, radar, generated, method, Myron, Fed, DDS, malfunctioning, predefined, indication, invention, reference, frequency, voltage, clock

   ... malfunctioning has been provided." The inventor was issued U.S. Patent No. **7,295,151** on Nov. 13. The patent has been assigned to Honeywell International ...
   ... r=1&f=G&l=50&s1=**7,295,151**.PN.&OS=PN/**7,295,151**&RS=PN/**7,295,151**.
   For more information about US Fed News ...

2. London Stock Exchange Aggregated Regulatory News Service (ARNS), February 5, 2009 Thursday 11:01 AM GMT, , 1514 words, Smiths News Plc Holding(s) in Company

   CORE TERMS: voting rights, voting, PLC, Indirect, PMC, issuer, financial instruments, ASSURANCE, PENSIONS, Percentage, notification, triggering, attached, Holdings Limited, Resulting, LGPL, LGAS, threshold, disposal, notified, crossed, filings, annex

   ... | GBP 0.05 | 8,304,531 | 8,304,531 | **7,295,151** | **7,295,151** | | 3.98 |
   ... | **7,295,151** | 3.98 | ...
   ... Legal & General Group Plc (Direct)(L&G) (**7,295,151** - 3.98% = LGAS,LGPL & PMC) Le General Investment ...

3. London Stock Exchange Aggregated Regulatory News Service (ARNS), March 13, 2009 Fri 5:48 PM GMT, , 1655 words, Smiths News Plc Holding(s) in Company

   CORE TERMS: voting rights, PLC, Indirect, PMC, issuer, voting, financial instruments, Percentage, Assurance, notification, triggering, attached, Holdings Limited, Resulting, Pen LGPL, LGAS, threshold, disposal, notified, crossed, filings, annex

   ... | ORD | **7,295,151** | **7,295,151** | 8,278,486 | 8,278,486 | | 4.52 |

4. Targeted News Service, November 14, 2007 Wednesday 2:04 PM EST, , 2670 words, U.S. Patents Awarded to Inventors in Washington (Nov. 14), Targeted News service Targeted News Service, Alexandria, VA.

   CORE TERMS: patent, Wash, inventor, method, invention, Trademark Office, original application, nph-Parser, Sect1, Sect2, HITOFF, PTO1, PALL, netacgi, patft, uspto, http, gov, htm, assigned, signal, lens, electro-active, abstract, Patent Office, surface, Redmond, portion, dynamic, ornamental

   ... malfunctioning has been provided." The inventor was issued U.S. Patent No.

7,295,151 on Nov. 13. The patent has been assigned to Honeywell International ...
... r=1&f=G&l=50&s1=**7,295,151**.PN.&OS=PN/**7,295,151**&RS=PN/**7,295,151**.
*** Washington Inventors Develop Ornamental Design for Display ...

Source: Command Searching > **News, All (English, Full Text)**
Terms: **7295151 or 7,295,151** (Edit Search)
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Date/Time: Thursday, June 17, 2010 - 2:42 PM EDT

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Washington Inventor Develops Radar Altimeter Self-Testing Method US Fed News November 14, 2007 Wednesday 1:05 AM EST

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US Fed News

November 14, 2007 Wednesday 1:05 AM EST

LENGTH: 267 words

HEADLINE: Washington Inventor Develops Radar Altimeter Self-Testing Method

BYLINE: US Fed News

DATELINE: Alexandria, Va.

BODY:

ALEXANDRIA, Va., Nov. 14 -- David C. Vacanti of Renton, Wash., has developed

a method for self-test of a radar altimeter.

According to the U.S. Patent & Trademark Office, the invention relates to "systems and methods for testing a signal generated by a direct digital synthesizer (DDS) in a radar altimeter. In an embodiment of the method, a voltage signal derived by comparing a fixed reference frequency to a ramped frequency signal generated by the DDS based on a clock-based reference signal is generated. The generated voltage signal is integrated over a predefined range of clock signals."

An abstract of the invention, released by the Patent Office, said: "The integration is sampled at a previously defined clock tick. The sample is compared to a desired value and an indication that the radar altimeter is malfunctioning is provided if the comparison exceeds a predefined threshold value. The radar altimeter system is deactivated if an indication that the radar altimeter is malfunctioning has been provided."

The inventor was issued U.S. Patent No. **7,295,151** on Nov. 13.

The patent has been assigned to Honeywell International Inc., Morristown, N.J.

The original application was filed on Dec. 19, 2005, and is available at:
http://patft.uspto.gov/netacgi/nph-Parser?Sect1=PTO1&Sect2=HITOFF&d=PALL&p=1&u=%

2Fnetahtml%2FPTO%
2Fsrchnum.htm&r=1&f=G&l=50&s1=**7,295,151.**PN.&OS=PN/**7,295,151**&RS=PN/**7,295,151**.

For more information about US Fed News federal patent awards please contact: Myron Struck, Managing Editor/US Bureau, US Fed News, Direct: 703/866-4708, Cell: 703/304-1897, Myron@targetednews.com

LOAD-DATE: November 14, 2007

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November 14, 2007 Wednesday 2:04 PM EST

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November 14, 2007 Wednesday 2:04 PM EST

LENGTH: 2670 words

HEADLINE: U.S. Patents Awarded to Inventors in Washington (Nov. 14)

BYLINE: Targeted News service Targeted News Service

DATELINE: Alexandria, VA.

BODY:

ALEXANDRIA, Va., Nov. 14 -- The following federal patents were awarded to inventors in Washington.

***

Washington Inventors Develop Print Gap Correction Method

ALEXANDRIA, Va., Nov. 14 -- Daniel A Durland and Gary A Gesellchen, both of Seattle, Scott D Phillips of Kent, Wash., Joel C Brown of Renton, Wash., Craig J Cornelius of Woodinville of Wash., Kenneth R Hallock of Buckley, Wash., and Christopher J Bakken of Auburn, Wash., have developed a line printer with a motorized platen.

According to the U.S. Patent & Trademark Office: "Methods for setting the print gap in a printer are disclosed. The printer has an eccentric platen wherein rotation of the platen changes the print gap distance. A driver controls the eccentric platen rotation, and a sensor may be used to determine the position of the motor and platen."

An abstract of the invention, released by the Patent Office, said: "The configuration of an eccentric platen and a driver having a position sensor enables measuring the thickness of a form at one or more locations to create a representative thickness profile of the form, which may be saved to the printer's computer memory and repeated when printing similar forms in the future, thus obviating the need to measure the thickness of every individual form."

The inventors were issued U.S. Patent No. 7,290,949 on Nov. 6.

The patent has been assigned to TallyGenicom LP, Kent.

The original application was filed on Oct. 12, 2005, and is available at: http://patft.uspto.gov/netacgi/nph-Parser?Sect1=PTO1&Sect2=HITOFF&d=PALL&p=1&u=%2Fnetahtml%2FPTO%2Fsrchnum.htm&r=1&f=G&l=50&s1=7,290,949.PN.&OS=PN/7,290,949&RS=PN/7,290,949.

***

California, Washington Inventors Develop Electro-Active Spectacle Lens Designing Method

ALEXANDRIA, Va., Nov. 14 -- Dwight P. Duston of Laguna Niguel, Calif., and William Kokonaski of Gig Harbor, Wash., have developed an electro-active multi-focal spectacle lens.

According to the U.S. Patent & Trademark Office, the invention relates to a "method and system for designing electro-active lenses adapted to correct for a higher-order aberration, whereby a desired pixel size is determined. In certain embodiments, the desired pixel size may be determined by identifying a desired pixel size for an electro-active lens adapted to correct for a conventional aberration."

The inventors were issued U.S. Patent No. 7,290,876 on Nov. 6.

The patent has been assigned to E-Vision LLC, Roanoke, Va.

The original application was filed on July 18, 2005, and is available at: http://patft.uspto.gov/netacgi/nph-Parser?Sect1=PTO1&Sect2=HITOFF&d=PALL&p=1&u=%2Fnetahtml%2FPTO%2Fsrchnum.htm&r=1&f=G&l=50&s1=7,290,876.PN.&OS=PN/7,290,876&RS=PN/7,290,876.

***

Virginia, Washington Inventors Develop Electro-Active Spectacle

ALEXANDRIA, Va., Nov. 14 -- Ronald D. Blum of Roanoke, Va., and William Kokonaski of Gig Harbor, Wash., have developed an electro-active spectacles fabricating method.

According to the U.S. Patent & Trademark Office: "A composite lens assembly comprising an electro-active lens assembly, a first lens wafer, and a second lens wafer is provided. The electro-active lens assembly has an upper substrate layer with a planar upper surface and a lower substrate layer with a planar lower surface."

An abstract of the invention, released by the Patent Office, said: "The first lens wafer has a planar lower wafer surface adjacent and parallel to the planar upper surface of the upper substrate layer of the electro-active lens assembly. The second lens wafer has a planar upper wafer surface adjacent and parallel to the planar lower surface of the lower substrate layer of the electro-active lens assembly."

The inventors were issued U.S. Patent No. 7,290,875 on Nov. 6.

The original application was filed on Oct. 31, 2005, and is available at: http://patft.uspto.gov/netacgi/nph-Parser?Sect1=PTO1&Sect2=HITOFF&d=PALL&p=1&u=%2Fnetahtml%2FPTO%2Fsrchnum.htm&r=1&f=G&l=50&s1=7,290,875.PN.&OS=PN/7,290,875&RS=PN/7,290,875.

***

Washington Inventor Develops Ophthalmic Surgery Simulation Device

ALEXANDRIA, Va., Nov. 14 -- Clifton S. Otto of Lakewood, Wash., has developed an ophthalmic surgery simulation device.

According to the U.S. Patent & Trademark Office: "This invention relates to a device for simulating life-like conditions during eye surgery. The device includes a prosthetic head which includes an eye socket coupled to a fluid line adapted to create negative pressure or a vacuum in the eye socket."

An abstract of the invention, released by the Patent Office, said: "An eye for practicing surgical techniques can be placed in the socket and negative pressure used to hold the eye in the socket in a manner which feels analogous to the way an eye feels to a surgeon operating on a live human."

The inventor was issued U.S. Patent No. 7,291,016 on Nov. 6.

The patent has been assigned to U.S. Army, Washington.

The original application was filed on Jan. 7, 2005, and is available at: http://patft.uspto.gov/netacgi/nph-Parser?Sect1=PTO1&Sect2=HITOFF&d=PALL&p=1&u=%2Fnetahtml%2FPTO%2Fsrchnum.htm&r=1&f=G&l=50&s1=7,291,016.PN.&OS=PN/7,291,016&RS=PN/7,291,016.

***

Washington Inventors Develop Ornamental Design for Display Screen Portion Icon

ALEXANDRIA, Va., Nov. 14 -- Niels Van Dongen and Kieran Phelan, both of Seattle, have developed an ornamental design for an icon for a portion of a display screen, the U.S. Patent & Trademark Office announced.

The inventors were issued U.S. Patent No. D554,664 on Nov. 6.

The patent has been assigned to Microsoft Corp., Redmond, Wash.

The original application was filed on Feb. 7, 2006, and is available at: http://patft.uspto.gov/netacgi/nph-Parser?Sect1=PTO1&Sect2=HITOFF&d=PALL&p=1&u=%2Fnetahtml%2FPTO%2Fsrchnum.htm&r=1&f=G&l=50&s1=D554,664.PN.&OS=PN/D554,664&RS=PN/D554,664.

***

Washington Inventor Develops Radar Altimeter Self-Testing Method

ALEXANDRIA, Va., Nov. 14 -- David C. Vacanti of Renton, Wash., has developed

a method for self-test of a radar altimeter.

According to the U.S. Patent & Trademark Office, the invention relates to "systems and methods for testing a signal generated by a direct digital synthesizer (DDS) in a radar altimeter. In an embodiment of the method, a voltage signal derived by comparing a fixed reference frequency to a ramped frequency signal generated by the DDS based on a clock-based reference signal is generated. The generated voltage signal is integrated over a

predefined range of clock signals."

An abstract of the invention, released by the Patent Office, said: "The integration is sampled at a previously defined clock tick. The sample is compared to a desired value and an indication that the radar altimeter is malfunctioning is provided if the comparison exceeds a predefined threshold value. The radar altimeter system is deactivated if an indication that the radar altimeter is malfunctioning has been provided."

The inventor was issued U.S. Patent No. **7,295,151** on Nov. 13.

The patent has been assigned to Honeywell International Inc., Morristown, N.J.

The original application was filed on Dec. 19, 2005, and is available at: http://patft.uspto.gov/netacgi/nph-Parser?Sect1=PTO1&Sect2=HITOFF&d=PALL&p=1&u=%2Fnetahtml%2FPTO%2Fsrchnum.htm&r=1&f=G&l=50&s1=**7,295,151.**PN.&OS=PN/**7,295,151**&RS=PN/**7,295,151**.

***

Washington Inventors Develop Ornamental Design for Display Screen Portion Image

ALEXANDRIA, Va., Nov. 14 -- Niels Van Dongen and Emily Rimas-Ribikauskas, both of Seattle, Paul Hoover of Kirkland, Wash., and Dustin Hubbard of Sammamish, Wash., have developed an ornamental design for an image for a portion of a display screen, the U.S. Patent & Trademark Office announced.

The inventors were issued U.S. Patent No. D554,662 on Nov. 6.

The patent has been assigned to Microsoft Corp., Redmond, Wash.

The original application was filed on Nov. 14, 2005, and is available at: http://patft.uspto.gov/netacgi/nph-Parser?Sect1=PTO1&Sect2=HITOFF&d=PALL&p=1&u=%2Fnetahtml%2FPTO%2Fsrchnum.htm&r=1&f=G&l=50&s1=D554,662.PN.&OS=PN/D554,662&RS=PN/D554,662.

***

Washington Inventors Develop Holographic Array for Multi-Path Imaging Artifact Reduction

ALEXANDRIA, Va., Nov. 14 -- Douglas L. McMakin and David M. Sheen, both of Richland, Wash., and Thomas E. Hall of Kennewick, Wash., have developed a holographic array for multi-path imaging artifact reduction.

According to the U.S. Patent & Trademark Office, the invention relates to a "method and apparatus to remove human features utilizing at least one transmitter transmitting a signal between 200 megahertz and 1 terahertz, the signal having at least one characteristic of elliptical polarization, and at least one receiver receiving the reflection of the signal from the transmitter. A plurality of such receivers and transmitters are arranged together in an array which is in turn mounted to a scanner, allowing the array to be passed adjacent to the surface of the item being imaged while the transmitter is transmitting electromagnetic radiation."

An abstract of the invention, released by the Patent Office, said: "The array is passed adjacent to the surface of the item, such as a human being, that is being imaged. The portions of the received signals wherein the polarity of the characteristic has been reversed and those portions of the received signal wherein the polarity of the characteristic has not

been reversed are identified. An image of the item from those portions of the received signal wherein the polarity of the characteristic was reversed is then created."

The inventors were issued U.S. Patent No. 7,295,146 on Nov. 13.

The patent has been assigned to Battelle Memorial Institute, Richland.

The original application was filed on March 24, 2005, and is available at: http://patft.uspto.gov/netacgi/nph-Parser?Sect1=PTO1&Sect2=HITOFF&d=PALL&p=1&u=%2Fnetahtml%2FPTO%2Fsrchnum.htm&r=1&f=G&l=50&s1=7,295,146.PN.&OS=PN/7,295,146&RS=PN/7,295,146.

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Washington Inventors Develop Ornamental Design for Display Screen Graphical Identification Element

ALEXANDRIA, Va., Nov. 14 -- Jason A. Gold and Harris Thurmond, both of Seattle, Alan T. Shen of Redmond, Wash., and Jeffrey L. Allen of Sammamish, Wash., have developed an ornamental design for a graphical identification element for a display screen, the U.S. Patent & Trademark Office announced.

The inventors were issued U.S. Patent No. D554,652 on Nov. 6.

The patent has been assigned to Microsoft Corp., Redmond.

The original application was filed on July 13, 2006, and is available at: http://patft.uspto.gov/netacgi/nph-Parser?Sect1=PTO1&Sect2=HITOFF&d=PALL&p=1&u=%2Fnetahtml%2FPTO%2Fsrchnum.htm&r=1&f=G&l=50&s1=D554,652.PN.&OS=PN/D554,652&RS=PN/D554,652.

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Washington Inventors Develop Dynamic Properties Simulation Method

ALEXANDRIA, Va., Nov. 14 -- Daniel J.Miller of Carnation, Wash. and David M. Maymudes of Seattle, Wash., have developed a method to simulate dynamic properties.

According to the U.S. Patent & Trademark Office: "Methods and systems of simulating dynamic properties on computer-implemented objects that do not support dynamic properties are described. In one embodiment, one or more first objects that do not support dynamic properties are provided."

An abstract of the invention, released by the Patent Office, said: "One or more second programmable objects are provided and are programmed to effect property value changes on the objects that do not support dynamic properties."

The inventors were issued U.S. Patent No. 7,296,276 on Nov. 13.

The patent has been assigned to Microsoft Corp., Redmond, Wash.

The original application was filed on Aug. 26, 2004, and is available at: http://patft.uspto.gov/netacgi/nph-Parser?Sect1=PTO1&Sect2=HITOFF&d=PALL&p=1&u=%2Fnetahtml%2FPTO%2Fsrchnum.htm&r=1&f=G&l=50&s1=7,296,276.PN.&OS=PN/7,296,276&RS=PN/7,296,276.

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Washington, Michigan Inventors Develop Load-Balancing Cluster Control Method

ALEXANDRIA, Va., Nov. 14 -- Christopher L.Darling of Sammamish, Wash., Hallvard S. Kaldestad of Carnation, Wash. and Howard A. Aikins of Monroe, Wash. and Michael E. Gernaey of Lapeer, Mich., have developed a method of controlling load-balancing cluster.

According to the U.S. Patent & Trademark Office: "An implementation of a technology, described herein, for remotely and dynamically monitoring the availability of the members of a load-balancing cluster. An implementation of the claimed invention includes a dynamic, exocluster application-layer monitor for dynamically monitoring and dynamically controlling the members of a load-balancing cluster. It may actively control the members that it monitors."

An abstract of the invention, released by the Patent Office, said: "The exocluster monitor is protocol agnostic. It dynamically adjusts so that it can monitor all of the members of the cluster as members are added and removed. This abstract itself is not intended to limit the scope of this patent. The scope of the present invention is pointed out in the appending claims."

The inventors were issued U.S. Patent No. 7,296,268 on Nov. 13.

The patent has been assigned to Microsoft Corp., Redmond, Wash.

The original application was filed on Dec. 18, 2000, and is available at:

http://patft.uspto.gov/netacgi/nph-Parser?Sect1=PTO1&Sect2=HITOFF&d=PALL&p=1&u=%2Fnetahtml%2FPTO%2Fsrchnum.htm&r=1&f=G&l=50&s1=7,296,268.PN.&OS=PN/7,296,268&RS=PN/7,296,268.

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West Virginia, Washington Inventors Develop Bis-Chelating Ligand

ALEXANDRIA, Va., Nov. 14 -- Wei-Jun Peng of Hurricane, W.Va., and Johnathan E Holladay of Kennewick, Wash., have developed a bis-chelating ligand and use thereof in carbonylation processes, the U.S. Patent & Trademark Office announced.

The inventors were issued U.S. Patent No. 7,294,729 on Nov. 13.

The patent has been assigned to Union Carbide Chemicals & Plastics Technology Corp., Danbury, Conn.

The original application was filed on Sept. 26, 2003, and is available at:
http://patft.uspto.gov/netacgi/nph-Parser?Sect1=PTO1&Sect2=HITOFF&d=PALL&p=1&u=%2Fnetahtml%2FPTO%2Fsrchnum.htm&r=1&f=G&l=50&s1=7,294,729.PN.&OS=PN/7,294,729&RS=PN/7,294,729.

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U.S., Indian Inventors Develop Radio Frequency Identification Method

ALEXANDRIA, Va., Nov. 14 -- Janaki Ram Goteti and Abhishek Agarwal, both of Hyderabad, India, Mohamed Fakrudeen Ali Ahmed of Coimbatore, India, Ramachandran Venkatesh of Bellevue, Wash., Anush Kumar of Seattle, Balasubramanian Sriram of Sammamish, Wash.,

have developed a radio frequency identification (RFID) method.

According to the U.S. Patent & Trademark Office: "The subject invention provides a system and/or a method that facilitates employing a model based at least upon a framework, wherein the model can be utilized to build an RFID application. A receiver component can receive data, wherein a model component can create a model based at least in part upon the received data that facilitates creating and/or executing the RFID application."

An abstract of the invention, released by the Patent Office, said: "The framework can be, but is not limited to, an entity, a framework class, a hierarchical framework of at least one class, an interface, an exception, a component architecture, a schema, an object model, and/or an Application Programming Interface. The model can be utilized to create, deploy, manage, and/or execute the RFID application to provide a generic operation and/or a business specific scenario."

The inventors were issued U.S. Patent No. 7,295,116 on Nov. 13.

The patent has been assigned to Microsoft Corp., Redmond, Wash.

The original application was filed on March 1, 2005, and is available at: http://patft.uspto.gov/netacgi/nph-Parser?Sect1=PTO1&Sect2=HITOFF&d=PALL&p=1&u=%2Fnetahtml%2FPTO%2Fsrchnum.htm&r=1&f=G&l=50&s1=7,295,116.PN.&OS=PN/7,295,116&RS=PN/7,295,116.

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